

UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO. : 6,812,530 B2
DATED : November 2, 2004
INVENTOR(S) : Schuegraf et al.

Page 1 of 1

It is certified that error appears in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

Title page,

Item [56], **References Cited**, FOREIGN PATENT DOCUMENTS, please insert

-- JP 401276761-A 11/1989 -- before

"JP 02265248 A 4/1989".

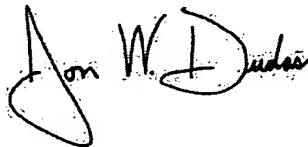
OTHER PUBLICATIONS, please insert -- Wolf et al., "Silicon Processing for the VLSI Era - Volume 1: Process Technology," © 1986 Lattice Press, pages 384-385 (4 pages total) -- after "Lee, B. et al." reference.

Column 6,

Lines 26 and 62, please delete "suicide" after "comprising" and insert -- silicide --.

Signed and Sealed this

Fourth Day of October, 2005

A handwritten signature in black ink, appearing to read "Jon W. Dudas". The signature is stylized with a large, looped initial "J" and a distinct "D".

JON W. DUDAS
Director of the United States Patent and Trademark Office